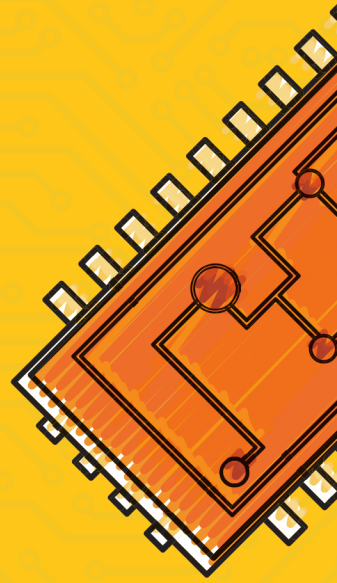
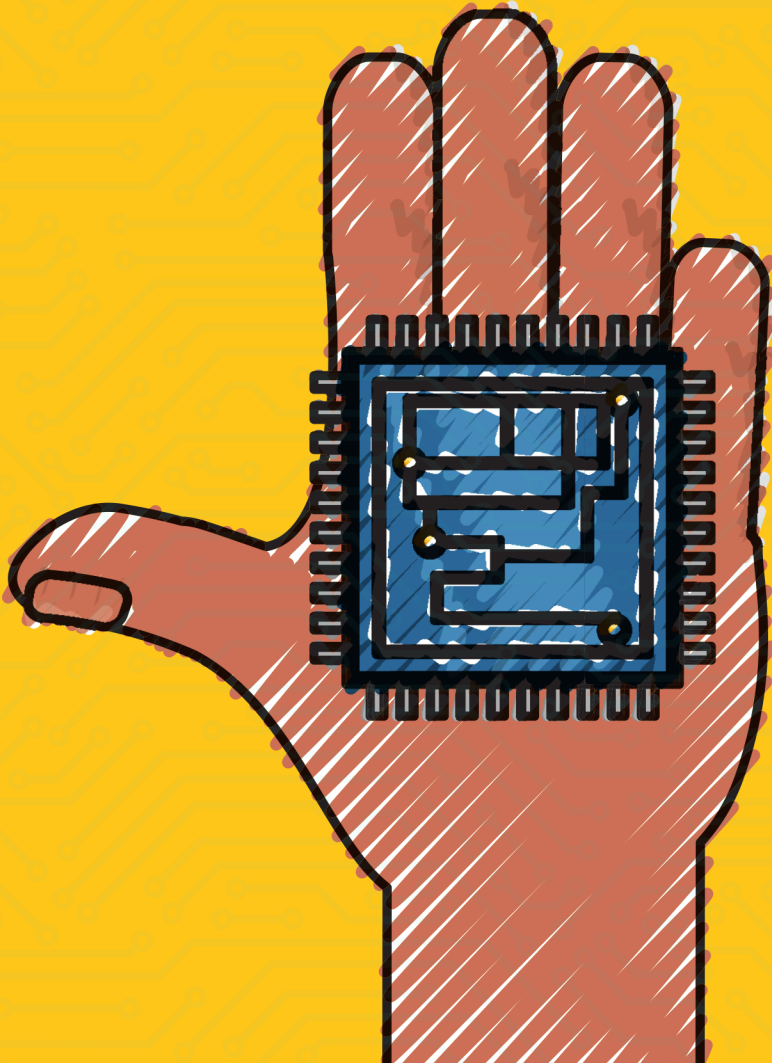













BITS & BYTES



THE OFFICIAL NEWSLETTER OF ELECTRONICS AND TELECOMMUNICATION BRANCH



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VISION:

To prepare academically excellent, research oriented, competent resources through quality and value based education and to carry out research and technology in relevant field of engineering to meet the needs of current industries, society and nation

MISSION:



To provide students with the theoretical knowledge blended with practical skills thereby building core competence of students



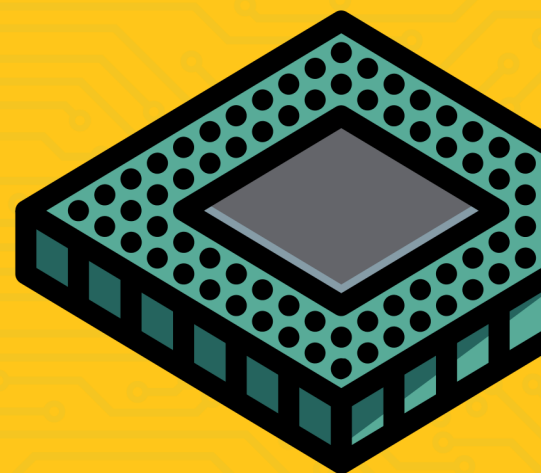
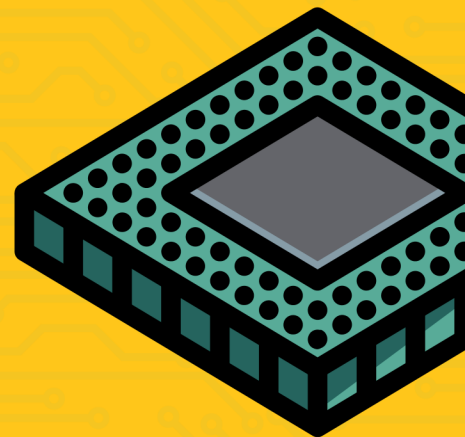
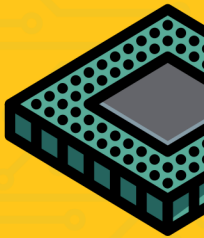
To build up leadership quality and professional creativity in our students



To carry out research through constant interaction with research organization and industry

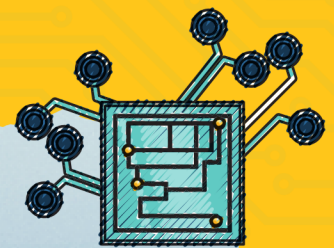


To enable students to organize various technical events which will bring out their inherent talents.



ABOUT THE DEPARTMENT

The Department of Electronics and Telecommunication Engineering (ETC) was established in the year 2009. The Department of ETC offers .B.Tech programme (4years) in Electronics and Telecommunication Engineering. Since the inception, the Department offers wide range of courses which address almost the entire arena covering VLSI, Microwave engineering, Communication, linear electronics, signal processing to mention a few. The department aims to create competent Electronics and Telecommunication engineers to serve the needs of modern advance technology and the nation as a whole. To compete these the department has well equipped laboratories like Electronics Device lab, analog electronics lab, VLSI lab, Microprocessor, Embedded system lab, Optical Communication lab, Communication lab, measurement lab mentioning a few with various modern software's and instruments. Keeping in mind of day-to-day advancement in electronics technology, the syllabus of the programme has been updated with new credit system (Credit Based Assessment System) following the model syllabus provided by AICTE. Students of ETC do their internship training at different organizations like BSNL, OIL, ONGC, NRL, in research labs etc. Passed out students have been successfully placed in different leading organizations like Capgemini, TCS , Wipro, Zaloni etc with some of the students have pursued higher studies in IITs, IIMs and in abroad.





FROM HOD'S DESK

"I am very much pleased to announce that the Department of Electronics and Telecommunication Engineering has released its 4th Issue of Bits and Bytes (Vol 1, Issue 4). The various academic activities as well as the achievements of our faculty members, students and alumni are presented in this newsletter. I am very happy to share that our students introduced all the clubs of JIST to the freshers in the ongoing Induction Program 2022. I hope this would ignite the fires of enthusiasm in students minds and generate fervor for innovative research ideas. I thank all the faculty members, staff members, students, alumni for their support in the growth and development of the department. I congratulate the editorial team and wish them luck."

Dr. Sanyukta Chetia

Asstt. Prof. & HoD



DEPARTMENTAL ACTIVITIES



- CIE-3 for the 4th & 6th semesters were held on 4th, 5th and 6th of July 2022.
- 6th semester General Viva-II, GAPBR-II & Mini -Project were held on 7th, 11th & 12th of July 2022.
- 4th Semester lab exams, General Viva -I & GAPBR-I were held on 7th, 8th & 9th of July 2022
- 2nd Semester End term Examination were held between 26th of July to 16th of August 2022.
- 4th semester End term Examinations were held between 25th of July to 11th August 2022
- 6th Semester End term Examinations were held between 22nd of July to 10th of August 2022
- Mr. Pranjal Das has successfully completed his H.O.D tenure & Dr. Sanyukta Chetia appointed as the new H.O.D of the ETC Dept on 25th of July 2022.
- A department meeting was held on 4th of August 2022 to discuss about the academic activities.
- All faculty members actively participated in the “Har Ghar Tiranga” movement by uploading selfies & pinning flag on the occasion of 75th “Azadi Ka Amrit Mahatsav”.
- Classes of 3rd, 5th & 7th Semester were started from 16th of September.
- Guide allotment for the final year project was done on 30th of September 2022.



FACULTY ACTIVITIES



- Dr. Sanyukta Chetia attended an AICTE Sponsored Two-Weeks Faculty Development Program “Bioengineering: A New Perspective Towards Multidisciplinary Research” organized by Department of Electronics and Communication Engineering, Don Bosco College of Engineering and Technology, Assam Don Bosco University, Guwahati-17, Assam from 4th July 2022 to 16th July 2022.
- Mr. Pranjal Das Appointed as Coordinator, CEE and JLEE 2022, JIST (30.08.2022).
- Ms. Kabita Chaudhury appointed as Verification Officer, CEE and JLEE counselling, JIST (30.08.2022).
- A paper on "Flexibility of Key Electronic and Optical Properties of Reduced Graphene Oxide Through Its Controlled Synthesis." Authored by Narzary Rewrewa, Palash Phukan, Satyajit Das, and Partha Pratim Sahu was published in IEEE Transactions on Electron Devices 69, no. 11 (2022): 6400-6407 on 21st September'22
- Mr. Satyajit Das has attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, Organized by IEEE EDS Student Branch Chapter MNIT, Jaipur.
- Dr. Sanyukta Chetia has attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, Organized by IEEE EDS Student Branch Chapter MNIT, Jaipur.

FACULTY ACTIVITIES



- Mrs. Kabita Chaudhury attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, organized by IEEE EDS Student Branch Chapter MNIT, Jaipur.
- Mrs. Bhagyashree Deka has attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, Organized by IEEE EDS Student Branch Chapter MNIT, Jaipur.
- Ms. Swati Singh has attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, Organized by IEEE EDS Student Branch Chapter MNIT, Jaipur.
- Ms. Sikha Swarnakar has attended Distinguished Lecture on " FOSS TCAD/EDA Tools SPICE and Verilog-A Modeling Flow" on September 22, 2022, Organized by IEEE EDS Student Branch Chapter MNIT, Jaipur



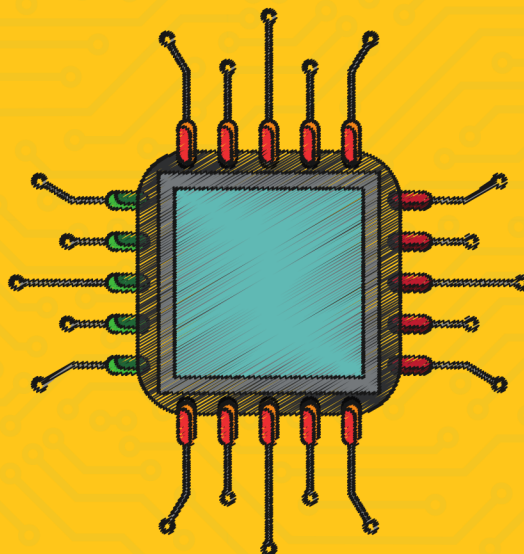
IIT-GUWAHATI GETS NORTHEAST'S FASTEST SUPERCOMPUTER

President Droupadi Murmu on October 13, 2022 commissioned the northeast's fastest supercomputer Param-Kamrupa, a high-performance computing cluster, which will give a boost to diverse fields from healthcare to advanced weather prediction.

STUDENT ACTIVITIES



- Pragyan Sen Deka and Rahul Prasad student of 5th Semester participated in National Terrain Treader event organised by NIT Silchar at their techfest from 20th to 23rd October and secured 3rd position in it.
- Manjit Kalita of 7th Semester and Anubhab Sarma of 5th Semester was selected as an Innovation Coordinator in IIC JIST.
- Chandan Rajbongshi of 7th Semester and Jishantu Kripal Bordoloi of 5th Semester was selected as a Start-up Coordinator in IIC JIST.
- Srishti Suman Nath of 7th Semester and Krishna Kumar Das of 5th Semester was selected as an Internship Coordinator in IIC JIST.
- Riznaz Hussain of 7th Semester and Prantik Das of 5th Semester was selected as an IPR Coordinator in IIC JIST.
- Raktim Deb of 7th Semester and Bitupan Bishal Porh of 5th Semester was selected as a Social Media Coordinator in IIC JIST.
- Riki Phukon of 7th Semester, Ayan Aabir Sarmah of 5th Semester and Polakshi Thakur of 3rd Semester was selected as a General Member in IIC JIST.





ALUMNI SECTION



- Jubair Ahmed of Batch 2019 and Jitul Shivam of Batch 2017 cleared APSC 2022.
- Adil Zafar Laskar, an alumnus of 2020 batch completed his M.Tech Program (Bioelectronics) under Tezpur University.



VEDANTA PICKS GUJARAT TO SET UP SEMICONDUCTOR MANUFACTURING UNIT; INVESTS RS 1.54 LAKH CRORE

Vedanta-Foxconn would invest Rs 1,54,000 crore to set up the facility in Gujarat, which would create one lakh job opportunities, Chief Minister Bhupendra Patel said after the MoU was signed in the presence of Union Minister for Railways, Communications, Electronics & Information Technology, Ashwini Vaishnaw, at a function in Gandhinagar.



GALLERY SECTION



IIC EVENTS 2022

GALLERY SECTION



FAREWELL

GALLERY SECTION



IDEATHON 2022

GALLERY SECTION



INDUCTION PROGRAM

EDITORIAL BOARD

ADVISOR



Mrs Bhagyashree Deka
Assistant Professor,ETC



Mrs Kabita Chaudhary
Assistant Professor,ETC

STUDENT MEMBERS



Chandan Rajbongshi
7th sem,ETC



Manjit Kalita
7th sem,ETC



Anubhab Sarma
5th sem,ETC